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Bib Data Sheet

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**\*\* CONTINUING DATA \*\*\*\*\*** *E. J. K.*  
 This application is a DIV of 10/010,504 12/05/2001 PAT 6,762,611

**\*\* FOREIGN APPLICATIONS \*\*\*\*\*** *E. J. K.*  
 GERMANY 100 60 438.2 12/05/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
**\*\* 03/24/2004**

|   |                                |                        |                      |                            |
|---|--------------------------------|------------------------|----------------------|----------------------------|
| Foreign Priority claimed<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no   | STATE OR<br>COUNTRY<br>GERMANY | SHEETS<br>DRAWING<br>4 | TOTAL<br>CLAIMS<br>6 | INDEPENDENT<br>CLAIMS<br>1 |
| 35 USC 119 (a-d) conditions met<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance |                                |                        |                      |                            |
| Verified and Acknowledged<br>Examiner's Signature <i>E. J. K.</i> Initials  |                                |                        |                      |                            |

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TITLE  
 Method and probe card configuration for testing a plurality of integrated circuits in parallel

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|-----------------------------------|---|---|
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